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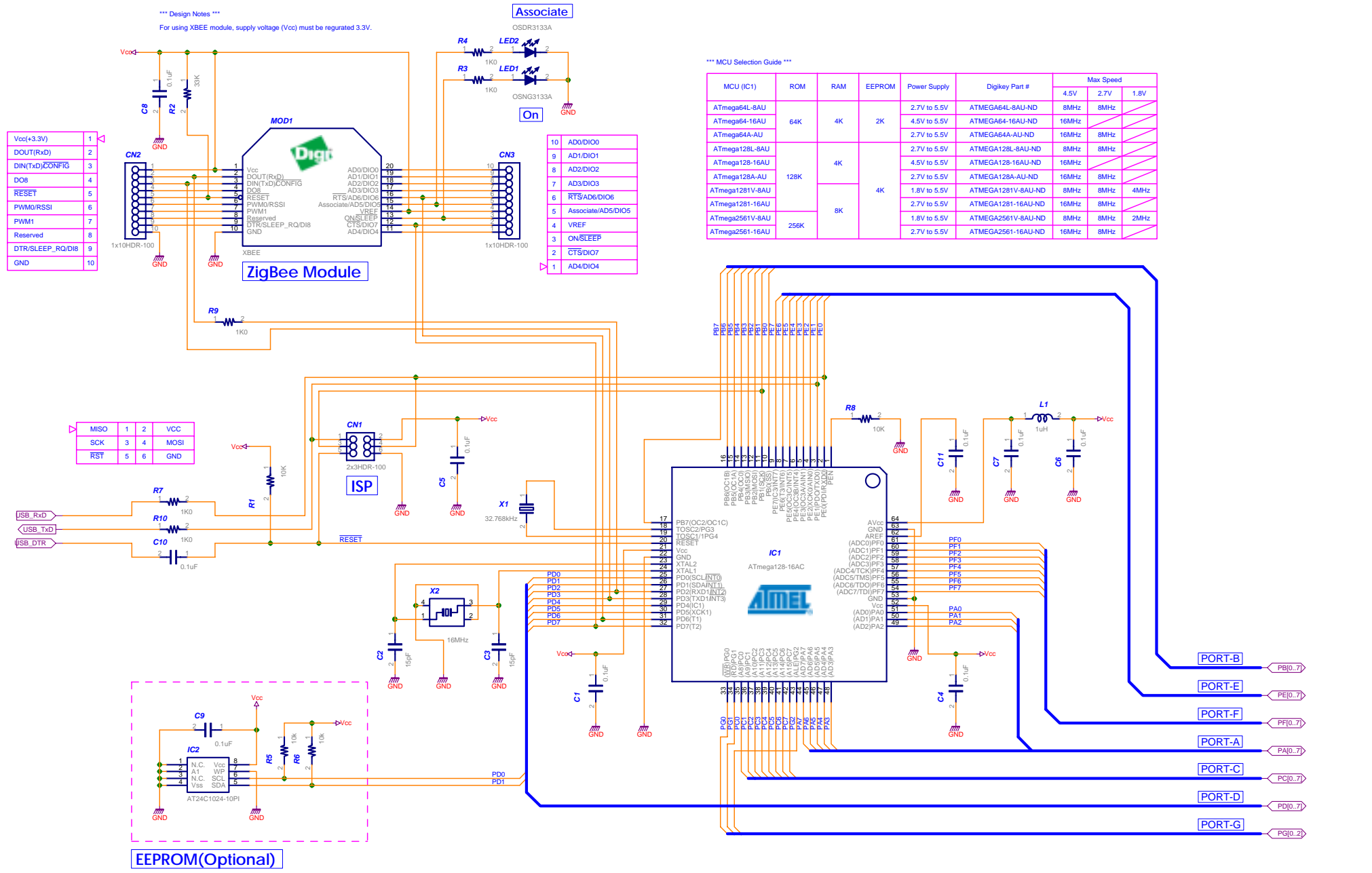
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NANDethno Pocket Mega #1 (ALL)

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PF7(ADC7/TDI)	14	13	PF6(ADC6/TDO)
PF5(ADC5/TMS)	12	11	PF4(ADC4/TCK)
PF3(ADC3)	10	9	PF2(ADC2)
PF1(ADC1)	8	7	PF0(ADC0)
PG2(ALE)	6	5	PG1(RD)
PG0(WR)	4	3	N.C.
VCC	2	1	GND

GND	1
PA0(AD0)	2
PA1(AD1)	3
PA2(AD2)	4
PA3(AD3)	5
PA4(AD4)	6
PA5(AD5)	7
PA6(AD6)	8
PA7(AD7)	9
Vcc	10

Vcc	10
PC7(A15)	9
PC6(A14)	8
PC5(A13)	7
PC4(A12)	6
PC3(A11)	5
PC2(A10)	4
PC1(A9)	3
PC0(A8)	2
GND	1

Port-F/Port-G

Port-A

Port-C

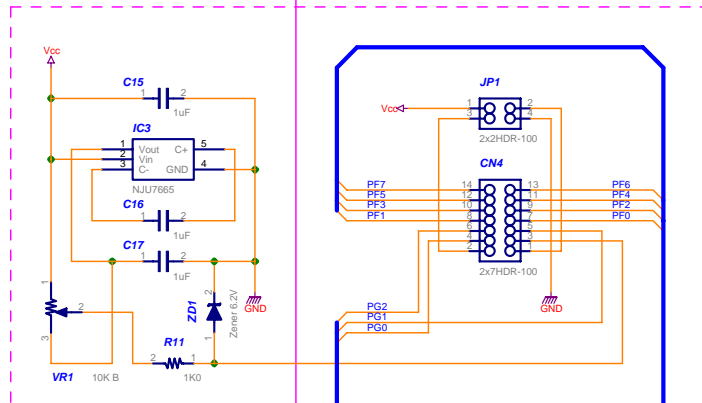
JP1 Setting for LCD type

16char x 2 lines 20char x 4 lines

DB7	14	13	DB6
DB5	12	11	DB4
DB3	10	9	DB2
DB1	8	7	DB0
E	6	5	R/W
PA0	4	3	VADJ
GND/VCC	2	1	VCC/GND

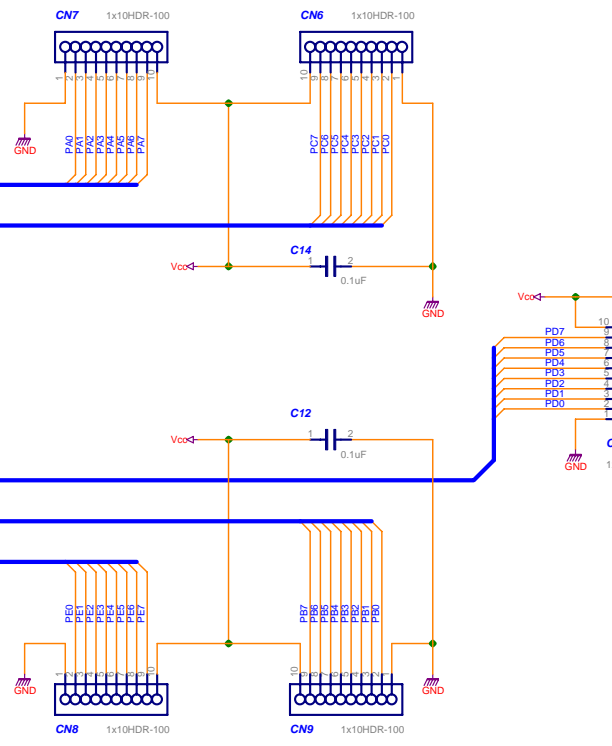
LCD-I/F

Contrast Adj.



DC-DC for LCD bias

- PORT-G
- PORT-F
- PORT-A
- PORT-C
- PORT-D
- PORT-B
- PORT-E



GND	1
PE8(C0/PDI)	2
PE1(XD0/PD0)	3
PE2(XC0/AIN0)	4
PE3(OCC3A/INT1)	5
PE4(OCC3B/INT4)	6
PE5(OCC3C/INT5)	7
PE6(T3/INT6)	8
PE7(IC3/INT7)	9
Vcc	10

Vcc	10
PB7(OCC0/C1)	9
PB6(OCC1B)	8
PB5(OCC1A)	7
PB4(OCC0)	6
PB3(MISO)	5
PB2(MOSI)	4
PB1(SCA)	3
PB0(SS)	2
GND	1

10	Vcc
9	PD7(T2)
8	PD6(T1)
7	PD5(XCK1)
6	PD4(ICP1)
5	PD3(TXD1/INT3)
4	PD2(RXD1/INT2)
3	PD1(SDA/INT1)
2	PD0(SCL/INT0)
1	GND

Port-D

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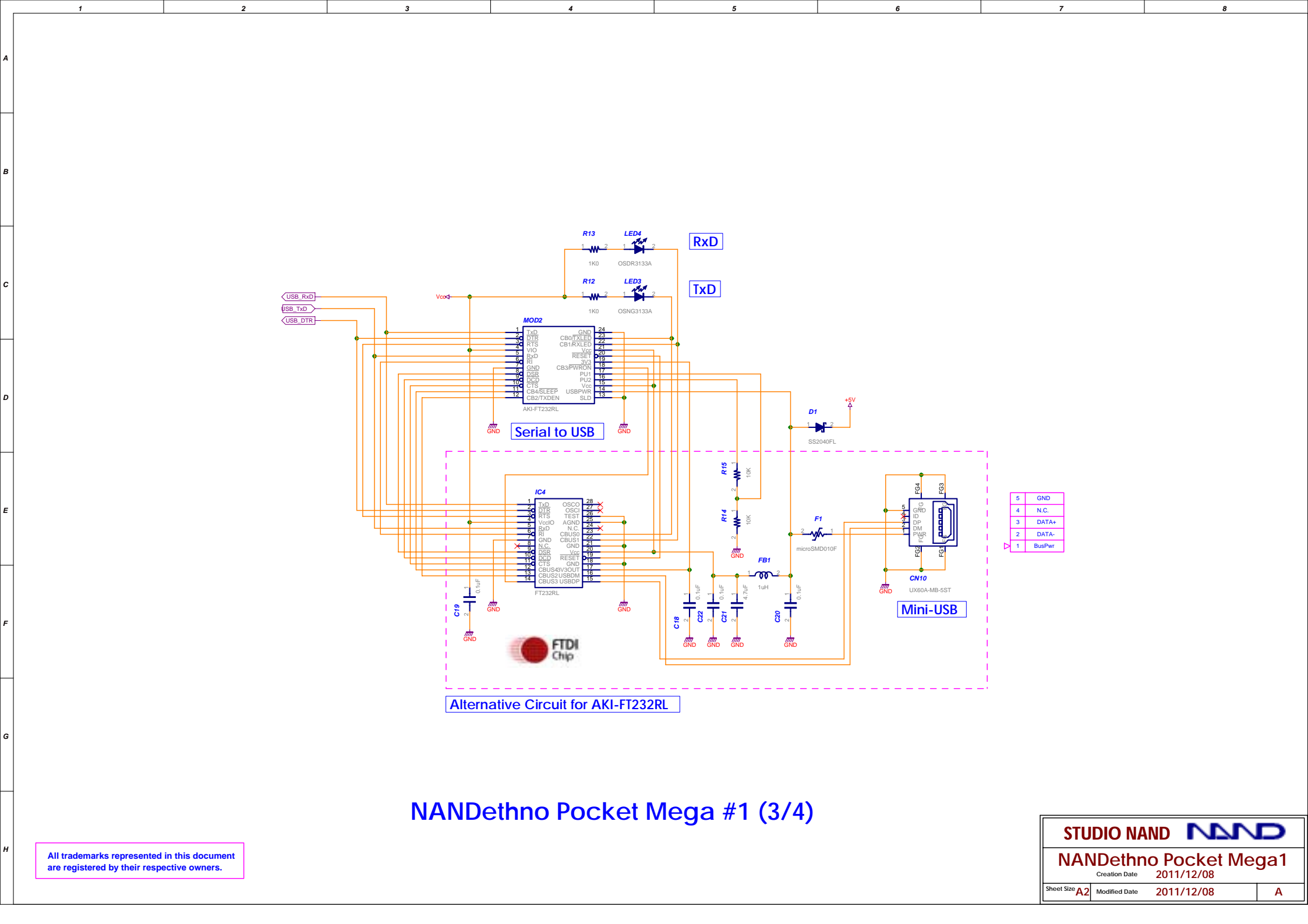
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5	GND
4	N.C.
3	DATA+
2	DATA-
1	BusPwr

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*** 3.3V LDO Regulator IC Selection Guide ***

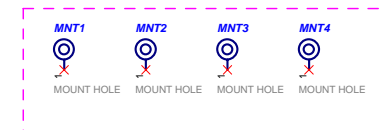
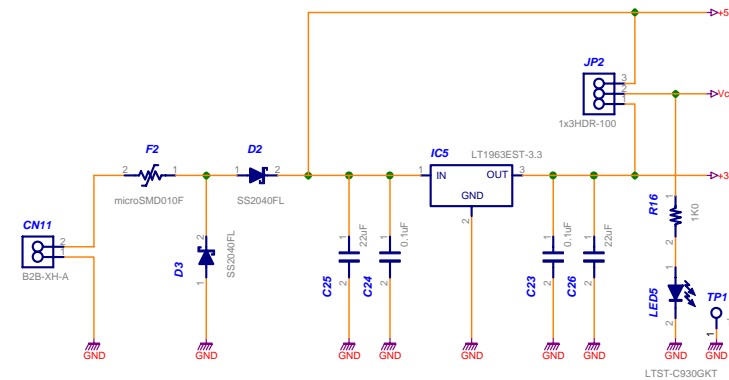
Order Part Number	Dropout Voltage	Output Current	MAX. Input Voltage
LT1129CST-3.3#PBF	400mV	700mA	+/-30V
LT1129IST-3.3#PBF	400mV	700mA	+/-30V
LT1963EST-3.3#PBF	340mV	1.5A	+/-20V
LT1963AEST-3.3#PBF	340mV	1.5A	+/-20V



JP2 Setting	Supply Voltage (Vcc)
	5V (USBPWR) 3.3V Reg. required
	3.3V 3.3V Reg. not required

*** Design Notes ***

For using XBEE module, supply voltage (Vcc) must be regulated 3.3V.
 Using as USB powered or 5V supply, on-board LDO is required, short 2-3 on JP2.
 If supply voltage is 3.3V, on-board LDO is not required, short 1-2 on JP2.
 When operating voltage (Vcc) is supplied from external, remove JP2 jumpers.



Mounting Holes

SYM1



NAND_LOGO

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