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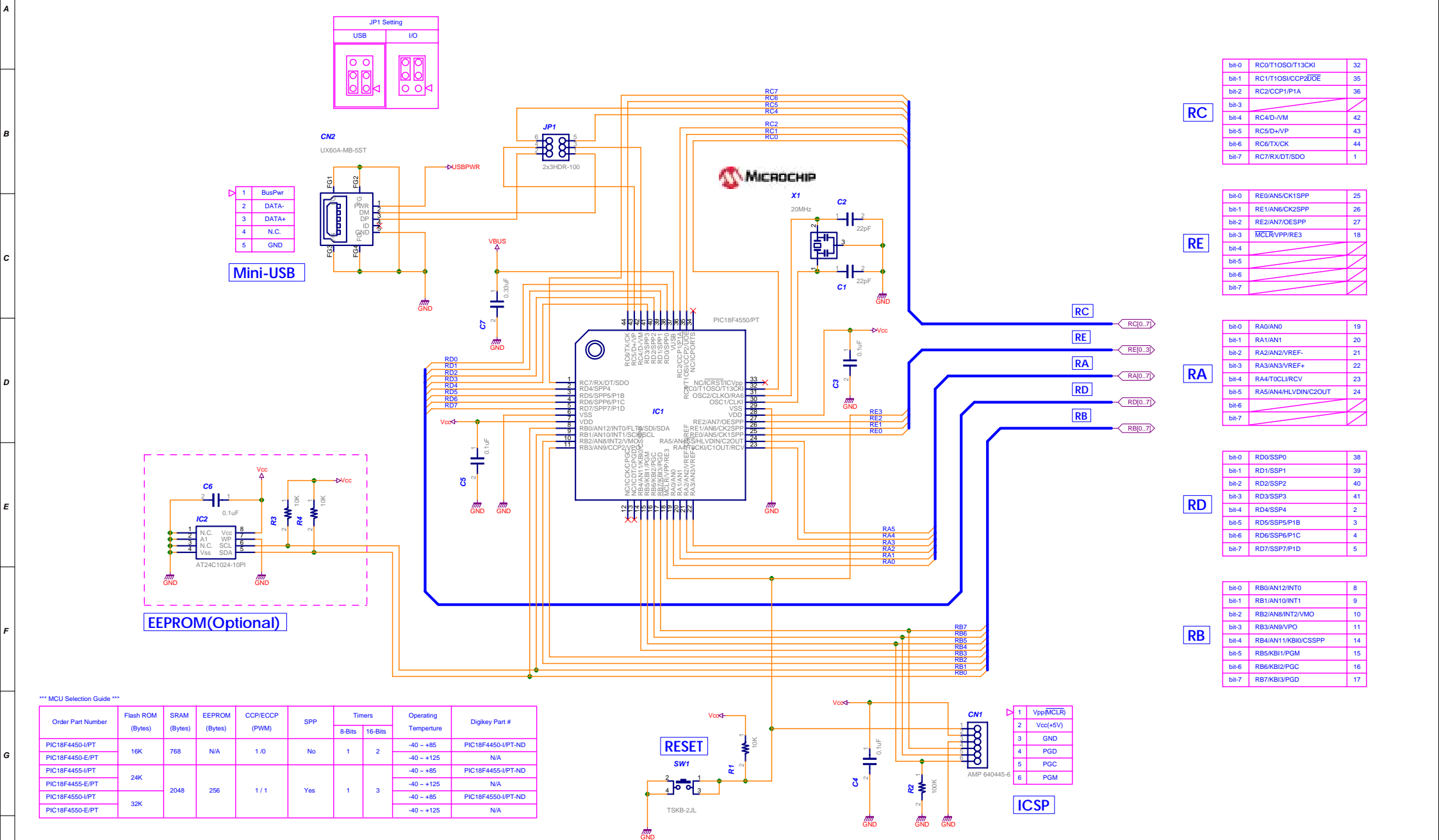
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# NANDethno Pocket PIC #2 (ALL)

## [ Generic Prototyping Board /w PIC18F4450/4550 Type-B ]

<b>STUDIO NAND</b> 	
<b>NANDethno Pocket PIC #2</b>	
<small>Creation Date</small> 2011/12/08	
<small>Sheet Size</small> A2	<small>Modified Date</small> 2011/12/08
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bit-0	RC0/T1OSO/T13CKI	32
bit-1	RC1/T1OSI/ICCP2UGE	35
bit-2	RC2/CCP1/P1A	36
bit-3		
bit-4	RC4/D-/VM	42
bit-5	RC5+/VP	43
bit-6	RC6/TX/CK	44
bit-7	RC7/RX/DT/SDO	1

bit-0	RE0/AN5/CK1SPP	25
bit-1	RE1/AN6/CK2SPP	26
bit-2	RE2/AN7/OESPP	27
bit-3	MCLR/VPP/RE3	18
bit-4		
bit-5		
bit-6		
bit-7		

bit-0	RA0/AN0	19
bit-1	RA1/AN1	20
bit-2	RA2/AN2/VREF-	21
bit-3	RA3/AN3/VREF+	22
bit-4	RA4/TOCLR/RCV	23
bit-5	RA5/AN4/HLVD/NC2OUT	24
bit-6		
bit-7		

bit-0	RD0/SSP0	38
bit-1	RD1/SSP1	39
bit-2	RD2/SSP2	40
bit-3	RD3/SSP3	41
bit-4	RD4/SSP4	2
bit-5	RD5/SSP5/P1B	3
bit-6	RD6/SSP6/P1C	4
bit-7	RD7/SSP7/P1D	5

bit-0	RB0/AN12/INT0	8
bit-1	RB1/AN10/INT1	9
bit-2	RB2/AN8/INT2/VM0	10
bit-3	RB3/AN9/VPO	11
bit-4	RB4/AN11/KBI0/CSSPP	14
bit-5	RB5/KBI1/PGM	15
bit-6	RB6/KBI2/PGC	16
bit-7	RB7/KBI3/PGD	17

\*\*\* MCU Selection Guide \*\*\*

Order Part Number	Flash ROM (Bytes)	SRAM (Bytes)	EEPROM (Bytes)	CCP/ECCP (PWM)	SPP	Timers		Operating Temperature	Digkey Part #
						8-Bits	16-Bits		
PIC18F4450-I/PT	16K	768	N/A	1/0	No	1	2	-40 ~ +85	PIC18F4450-I/PT-ND
PIC18F4450-E/PT						N/A			
PIC18F4455-I/PT	24K	2048	256	1/1	Yes	1	3	-40 ~ +85	PIC18F4455-I/PT-ND
PIC18F4455-E/PT						N/A			
PIC18F4450-I/PT	32K					1	3	-40 ~ +85	PIC18F4450-I/PT-ND
PIC18F4450-E/PT						N/A			

# NANDethno Pocket PIC #2 (1/3)

[ Generic Prototyping Board /w PIC18F4450/4550 Type-B ]

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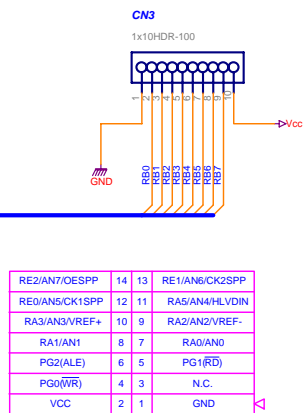
**NANDethno Pocket PIC #2**

Creation Date **2011/12/08**

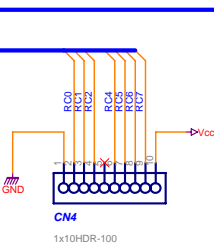
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1	GND
2	
3	
4	
5	
6	RA4/T0CLL/RCV
7	
8	RC6/TX/CK
9	RC7/RX/DT
10	Vcc(+3.3V/+5.0V)

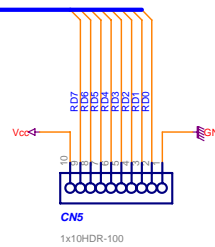
1	GND
2	RB0/AN12/INT0
3	RB1/AN10/INT1
4	RB2/AN8/INT2/VMO
5	RB3/AN6/VPO
6	RB4/AN11/KB0
7	RB5/KB1/PGM
8	RB6/KB2/PGC
9	RB7/KB3/PGD
10	Vcc(+3.3V/+5.0V)



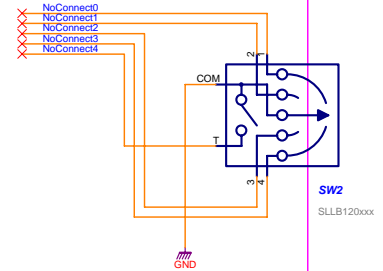
Port-RA/Port-RE



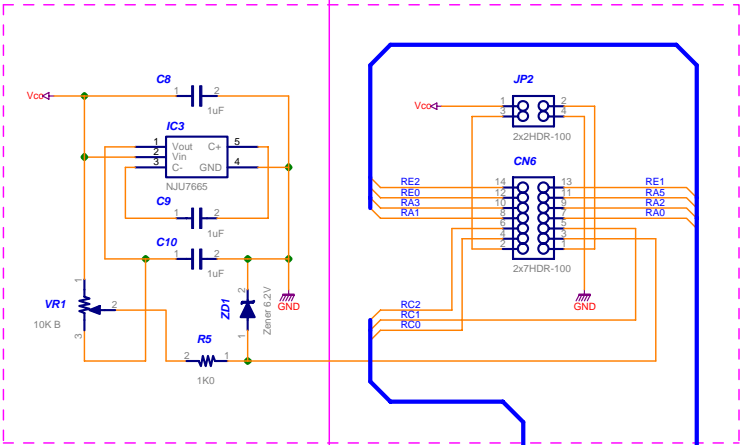
Port-RC



Port-RD

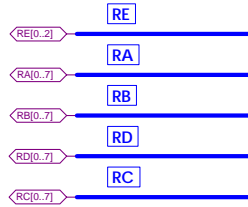


SW2  
SLLB120xxx



DC-DC for LCD bias

Contrast Adj.



# NANDethno Pocket PIC #2 (2/3)

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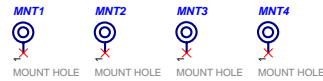
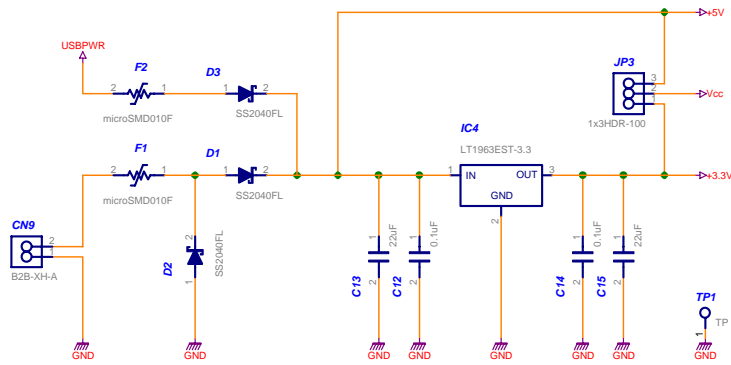
\*\*\* 3.3V LDO Regulator IC Selection Guide \*\*\*

Order Part Number	Dropout Voltage	Output Current	MAX. Input Voltage
LT1129CST-3.3#PBF	400mV	700mA	+/-30V
LT1129IST-3.3#PBF	400mV	700mA	+/-30V
LT1963EST-3.3#PBF	340mV	1.5A	+/-20V
LT1963AEST-3.3#PBF	340mV	1.5A	+/-20V

JP2 Setting	Supply Voltage (Vcc)
	5V (USBPWR) 3.3V Reg. required
	3.3V 3.3V Reg. not required

2	PWR
1	GND

EXT-PWR



Mounting Holes



\*\*\* Design Notes \*\*\*

For using XBEE module, supply voltage (Vcc) must be regulated 3.3V.  
Using as USB powered or 5V supply, on-board LDO is required, short 2-3 on JP2.  
If supply voltage is 3.3V, on-board LDO is not required, short 1-2 on JP2.  
When operating voltage (Vcc) is supplied from external, remove JP2 jumpers.



# NANDethno Pocket PIC #2 (3/3)

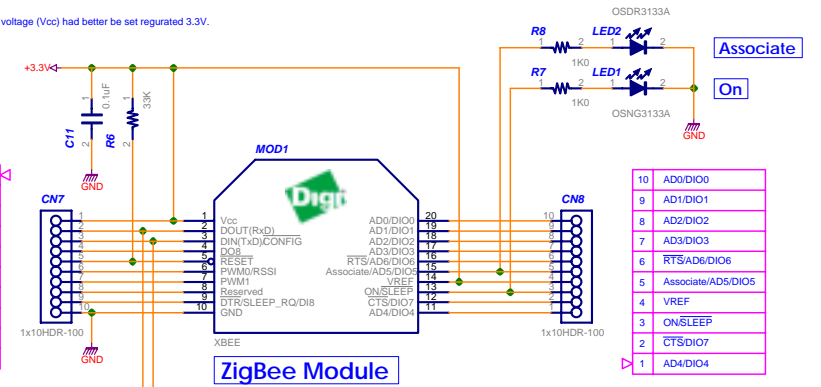
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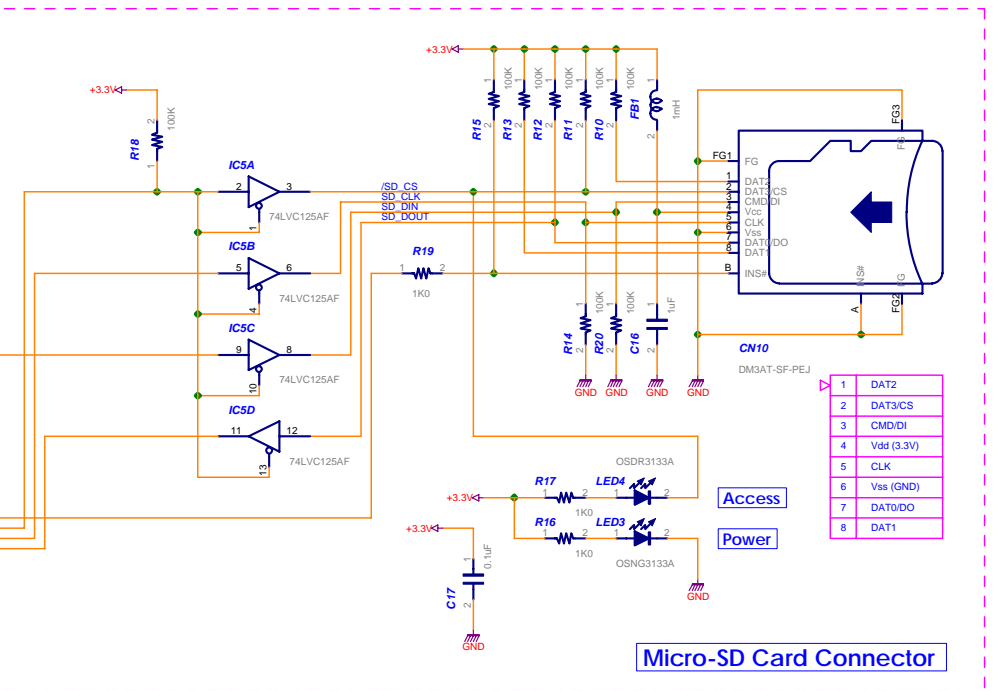
\*\*\* Design Notes \*\*\*

For using XBEE module, supply voltage (Vcc) had better be set regulated 3.3V.

Vcc(+3.3V)	1
DOUT(RxD)	2
DIN(TxD)/CONFIG	3
DO8	4
RESET	5
PWM0/RSSI	6
PWM1	7
Reserved	8
DTR/SLEEP_RQ/DI8	9
GND	10



10	AD0/DIO0
9	AD1/DIO1
8	AD2/DIO2
7	AD3/DIO3
6	RTS/AD6/DIO6
5	Associate/AD5/DIO5
4	VREF
3	ON/SLEEP
2	CTS/DIO7
1	AD4/DIO4



1	DAT2
2	DAT3/CS
3	CMDD/CI
4	Vdd (3.3V)
5	CLK
6	Vss (GND)
7	DAT0/DO
8	DAT1

Micro-SD Card Connector

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